



2184

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Dvorak et al.

Serial No.: 09/981,603

Filed: October 17, 2001

Title: ARC FAULT CIRCUIT
INTERRUPTER SYSTEM

Attny Dckt: NBD-48/47181-00259

Group Art: 2184

Examiner: Not Assigned

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FEB 08 2002

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§1.97 and 1.98

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Commissioner for Patents
Washington, D.C. 20231CERTIFICATE OF MAILING
37 C.F.R. 1.8I hereby certify that this correspondence is being deposited with the U.S.
Postal Service as First Class Mail in an envelope addressed to:
Commissioner for Patents, Washington, D.C. on the date indicated below:

Date

Corinne L. Roempagel

Dear Commissioner:

In compliance with the duty of disclosure under 37 C.F.R. §1.56, it is respectfully requested that this Information Disclosure Statement be entered and the references listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. § 1.98(d), copies of the listed references are enclosed.

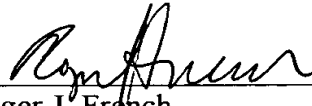
In accordance with 37 C.F.R. §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information disclosed is, or is considered to be, prior art with respect to the present application or material to patentability as defined in 37 C.F.R. §§ 1.56.

The present Information Disclosure statement is being filed within three months of the filing date of this application and hence is believed to be timely in accordance with 37 C.F.R. § 1.97(b). Accordingly, no fees are believed to be due in connection with the filing of this

Information Disclosure Statement. However, should any fees be deemed necessary (except payment of the issue fee), the Commissioner is authorized to charge any deficiency or to credit any over payment to Square D Company Account No. 19-3875/NBD-48.

Respectfully submitted,

January 17, 2002
Date



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